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7. (once amended) An I/O module comprising:

at least one connector pin, and

a control circuit comprising a plurality of solid state switches, said solid state switches controlling a configuration of the at least one pin.

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9. (once amended) An I/O module in accordance with Claim 8 wherein an energization state of each said at least one port controlling a state of a respective at least one switch.

PLEASE ADD THE FOLLOWING NEW CLAIMS:

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24. An I/O module comprising:

at least one connector pin; and

a control circuit comprising a plurality of switches controlling a configuration of said at least one pin and at least one port controlling a configuration of a respective at least one switch, an energization state of each said at least one port controlling a state of a respective at least one switch.

25. An I/O module comprising:

at least one connector pin; and

a control circuit comprising a plurality of switches controlling a configuration of said at least one pin, said circuit utilizing a single DAC for each said connector pin to implement a discrete digital mode and an analog mode.

26. An I/O module in accordance with Claim 25 wherein said circuit further comprising at least one port controlling a configuration of a respective at least one switch.

27. An I/O module in accordance with Claim 26 wherein an energization state of each said at least one port controlling a state of a respective at least one switch.

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28. A method for configuring at least one connector pin utilizing a control circuit, said method comprising controlling a configuration of the at least one connector pin utilizing a single DAC for each pin of the at least one connector pins to implement a discreet digital mode and an analog mode.

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29. A method in accordance with Claim 28 wherein the control circuit includes at least one port, said method further comprising providing an energization state to the at least one port to control a configuration of the at least one connector pin.

30. A method in accordance with Claim 29 wherein the control circuit includes at least one switch assembly including a switch, said method further comprising:

utilizing the energization state of the at least one port to control whether a respective at least one switch is in an open state or a closed state; and

controlling a configuration of the at least one connector pin utilizing the state of the at least one switch.

31. A method in accordance with Claim 30 wherein the at least one switch assembly includes at least one of a Pull-Down switch, a Pull-Up switch, a Discrete High switch, a Discrete Low switch, a positive 15 volt switch, a negative 15 volt switch, a range switch, and a voltage out switch.
